

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Satoshi KABURAKI et al.)
) Group Art Unit: Not assigned
Application No.: Not Yet Assigned)
) Examiner: Not assigned
Filed: January 8, 2004)
)
For: PROCESSOR, ARITHMETIC)
OPERATION PROCESSING)
METHOD, AND PRIORITY)
DETERMINATION METHOD)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicants bring to the Examiner's attention the documents listed on attached Form PTO-1449. Copies of the listed documents are attached. Applicants respectfully request that the Examiner consider the documents listed on attached Form PTO-1449 and indicate that they were considered by making an appropriate notation on this form.

This Information Disclosure Statement is being filed with the above-referenced application.

The following is a concise statement of relevance of the non-English language documents:

1. Japanese Patent Application No. 2003-029984 - discloses multi-thread techniques with multi-processors.

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2. Japanese Patent Application No. 2000-509528 - discloses a media control core (2 of Fig 1) that has an architecture to switch tasks. The relevance of this document is also discussed at page 2 of the specification of the present application.

3. Japanese Patent Application No. 3007612 - discloses a multi-task system which has some Hardware-Engines (HE). A transaction on an HE deals with the task. When the signal of the ended task occurs from the HE, micro-controller (101 of Fig. 1) catches the signal and switches the task to another.

4. Japanese Patent Application No. 2002-163121 -discloses an architecture to switch registers. (Fig 1).

An English language abstract of each document is enclosed.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim in the application and applicants determine that the cited documents do not constitute "prior art" under United States law, applicants reserve the right to present to the office the relevant facts and law regarding the appropriate status of such documents. Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please

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charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: January 8, 2004

By: 

Richard V. Burgujian
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Enclosures
RVB/FPD/dvg

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INFORMATION DISCLOSURE CITATION

Atty. Docket No. 02887.0263	Application No.
Applicant Satoshi KABURAKI et al.	
Filing Date January 8, 2004	Group: Not assigned

U.S. PATENT DOCUMENTS							
Examiner Initial*		Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS							
		Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
		2000-509528	7/25/00	Japan			Abstract
		2002-163121	6/7/02	Japan			Abstract
		3007612	11/26/99	Japan			Abstract
		2003-29984	1/31/03	Japan			Abstract

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	

Examiner	Date Considered
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce